



1150 McBride Avenue
Little Falls, New Jersey 07424-2500
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PRINCIPAL ENGINEER – SYSTEMS ELECTRONICS DESIGN & PROJECT LEAD

Responsibilities:

- Leads hardware development teams.
- Performs trade studies and error analyses.
- Establishes system architectures and detailed design requirements.
- Writes technical proposals.

Qualifications:

- At least 15 years experience in defense or aerospace electronics.
- Experience in analog and mixed-signal and digital design.
- Project leadership is required.
- Strong oral/written communication and presentation skills.
- Proficient in the use of software tools including Office and mathematical tools such as MathCad.
- BSEE; graduate degree or coursework preferable.

KEARFOTT CORPORATION Guidance & Navigation Division

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